ABSTRACT

An integrated circuit with a D.C./D.C. voltage regulator and its manufacturing process, including at least one power stage provided with at least two transistors and with at least one capacitor connecting a control electrode of the transistor to a reference voltage, a same control stage of the regulator providing a control signal of said transistors, the power stage being formed under a rail that provides supply signals of the integrated circuit, the rail providing at least two limiting supply voltages coming from the outside of the integrated circuit and at least one voltage regulated by the voltage regulator.

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